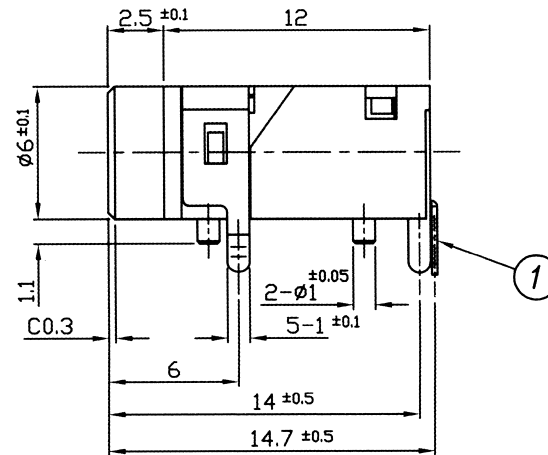
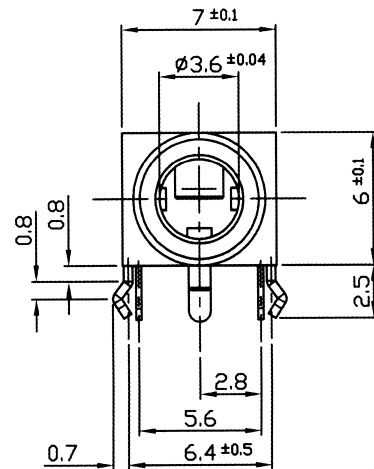
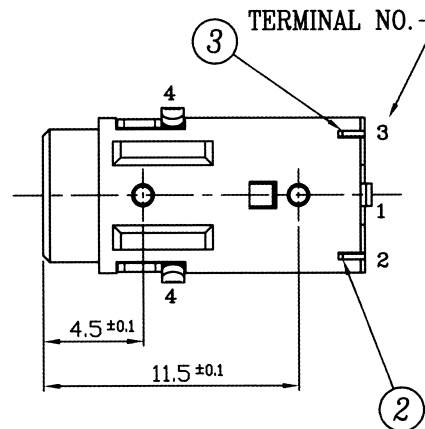
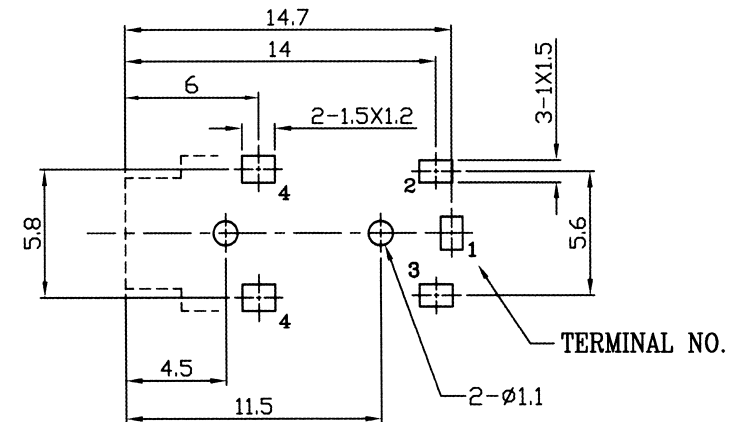


CIRCUIT DIAGRAM

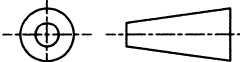


1. WITHSTANDING VOLTAGE : 500V AC FOR 1MINUTE
2. INSULATION RESISTANCE: 100MΩ MIN. AT 500V DC
3. CONTACT RESISTANCE: 30mΩ MAX



PC BOARD HOLE LAYOUT (Tols:±0.1)
(VIEW FROM DIRECTION OF INSERTION TO PCB)

5	BODY	1	PA66/GF RESIN	BLACK
4	CHIP SPRING	1	PBs 0.4t	Ag-PLATED
3	RING SPRING #3	1	PBs 0.25t	Ag-PLATED
2	RING SPRING #2	1	PBs 0.25t	Ag-PLATED
1	SLEEVE SPRING	1	PBs 0.25t	Ag-PLATED
LET	PART NAME	QTY	MATERIAL	REMARK

TOLERANCES X ≤ 5 = ± 0.2 X > 5 = ± 0.3 ANGULAR = ± --- DO NOT SCALE DWG.	PART NAME ø3.5 EARPHONE JACK		DWG NO. VER.1 MJ-2135
	MATL AS SHOWN IN TABLE		SCALE 3/1 mm
	DATE 16-08-2010		DRN / APPD. Y.H.TSANG/K.F.IM
FINISHED — — — — —	MARUSHIN ELECTRIC MFG.CO.,LTD.		